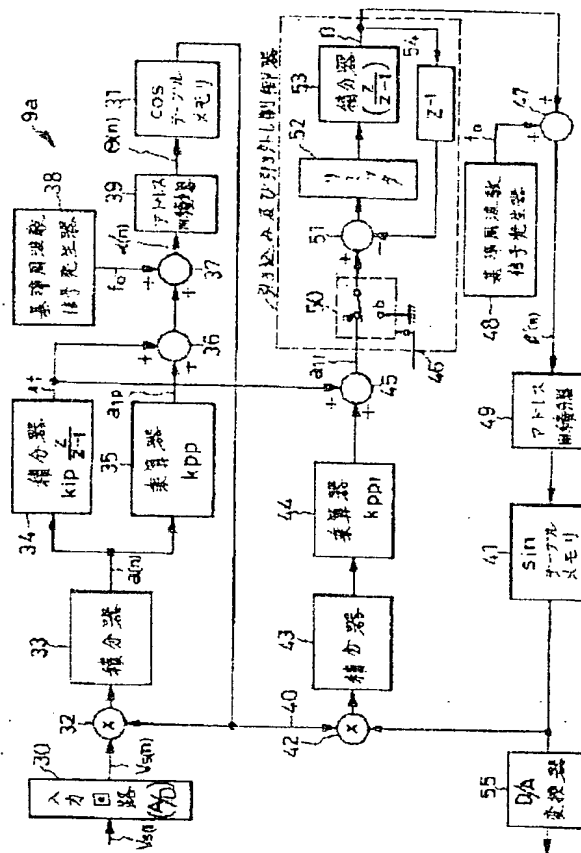


Patent Abstracts of Japan

TITLE : PERIODIC SIGNAL CONTROLLER



SOLUTION: A sine table memory 41 is provided, and sine data is read from the memory to obtain an output sine-wave signal in order to determine the output voltage waveform of an inverter. The frequency correction command value a_{11} corresponding to the difference between the frequency of a commercial alternating-current voltage and the output frequency, is found. The frequency correction command value a_{11} is limited through a limiter 52. When the frequency correction command value a_{11} is lower than the limit value of the limiter 52, sine data is read out without any change to the frequency correction command value a_{11} and taken as an amount β of frequency correction. When the frequency correction command value a_{11} is higher than the limit value of the limiter 52, the amount β of operation is varied with a constant gradient every sampling cycle.

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